

Generator of uniformly distributed random bits relates to computing, in particular to systems for protection of information of computing systems. In generator number of channels for formation of random bites can be $n \geq 2$. To each channel trigger-gates are additionally included, inputs of those are connected to outputs of counter triggers and outputs – to inputs of D-triggers. Synchronization inputs of trigger-gates are connected to output of generator of timing pulses. Technical result is in removal of meta-stable state in initial D-triggers without decrease of operation speed of the whole device, increase of number of channels to $n \geq 2$, increase of reliability of operation of the whole device and improvement of statistical properties of random bites generated.