

The high-voltage pulse capacitor comprises the case with a packet placed in it of shunted winded up flat sections located between flanges, the cover with low-inductance terminal placed on it consisting of central and external lead-in wire between them, electrically connected with the help of flat conductor lines. The section packet is arranged of two independent section packets fixed in the vertical position with the help of grooves and protrusive defects on the end face surface of each flange packet. The conductor lines adjoining to vertical parts of section packets, in the bottom part are arranged with a protrusive defect equal in length to low section packet height and have L-form. Their bottom part is made as a right-angle one. The conductor line of positive polarity in the place of connection to the central lead-in wire of the low-inductance connection terminal is immediately adjacent to the bottom surface of low-inductance connection terminal insulator.