

The proposed current mirror circuit contains $1 \dots m$ of the first transistors, $1 \dots n$ of the second transistors, and a third transistor. The bases of the first and the second transistors are connected together, and emitters are connected to the power supply terminal. The emitter of the third transistor is connected to the collectors of the second transistors, and the collector is used as the output of the circuit. The collectors and bases of the first transistors are connected together. Additionally, the circuit contains an element for providing voltage drop. The first lead of this element is connected to the collectors of the first transistors. The second lead of this element is connected to the base of the third transistor and is used as the output of the circuit. The current mirror circuit can contain $1 \dots k$ groups each containing $1 \dots s_k$ supplementary transistors. The bases of the supplementary transistors are connected to the bases of the first transistors, and the emitters are connected to the power supply terminal. In each group of the supplementary transistors, a transistor is provided which emitter is connected to the collectors of the transistors of the group, the base is connected to the second lead of the element for providing voltage drop, and the collector is used as the additional $(1 \dots k)$ th output of the circuit. As the element for providing voltage drop, one or several transistors connected in series can be used. In each of the said transistors, the collector and the base are connected together.