

The proposed differential amplifier contains an n-p-n transistor, a p-n-p transistor, and two double-base diodes. The collector lead of the n-p-n transistor is the first output lead of the amplifier. The collector lead of the p-n-p transistor is the second output lead of the amplifier. The p lead of the first double-base diode is connected to the n lead of the second double-base diode. The bases of both the double-base diodes are connected at the point that is used as the noninverting input of the amplifier. The point of connection of the first and the second double-base diodes p-n is used as the inverting input of the amplifier. The emitter of the n-p-n transistor and the n lead of the first double-base diode are connected at the point of the power source first terminal with the flowing in current. The emitter of the p-n-p transistor and the p lead of the second double-base diode are connected at the point of the power source first terminal with the flowing out current.