

The invention relates to a circuit arrangement with a number of electronic circuit components (2, 3, 4, 5). The operation state of said circuit arrangement can be changed, using a predetermined control signal C6, 7, 8, 9) to be applied to each circuit component (2, 3, 4, 5), to a reset or erased state in which the data contents of said circuit component (2, 3, 4, 5) adopt a logic zero. A control circuit (25) which is activated for successive resetting of the data contents of all circuit components (2, 3, 4, 5) to the logic zero and has a number, corresponding to the number of circuit components (2, 3, 4, 5), of opening stages (26, 27, 28, 29) connected in series. Each opening stage (26, 27, 28, 29) of the control circuit (25) is associated with each circuit component (2, 3, 4, 5) and is activated or driven to emit a control signal to the associated circuit components, with the exception of the first opening signal (42, 43, 44, 45) produced by the directly preceding opening stage (26, 27, 28, 29). After the associated circuit components (2, 3, 4, 5) have been reset, the opening stage (26, 27, 28, 29) emits an opening signal (42, 43, 44, 45) to control or activate the immediately following opening stage (26, 27, 28, 29).

